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Christian S. Collberg, Clark Thomborson, Gregg M. Townsend
October   ACM Transactions on Programming Languages and Systems
2007   (TOPLAS), Volume 29 Issue 6
Publisher: ACM
Full text available: ▨ pdf(1.48
MB)

Additional Information: full citation, abstract, references, index terms

Bibliometrics: Downloads (6 Weeks): 42,   Downloads (12 Months): 245,   Citation Count: 0

Fingerprinting embeds a secret message into a cover message. In media
fingerprinting, the secret is usually a copyright notice and the cover a digital image.
Fingerprinting an object discourages intellectual property theft, or when such theft
has occurred, ...

Keywords: Software piracy, software protection, watermarking

2 [HIDE: an infrastructure for efficiently protecting information leakage on the address bus](#)

Xiaotong Zhuang, Tao Zhang, Santosh Pande
December   ACM SIGOPS Operating Systems Review, Volume 38 Issue 5
2004
Publisher: ACM

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XOM-based secure processor has recently been introduced as a mechanism to provide copy and tamper resistant execution. XOM provides support for encryption/ decryption and integrity checking. However, neither XOM nor any other current approach adequately ...

Keywords: address bus leakage protection, secure processor

3 [HIDE: an infrastructure for efficiently protecting information leakage on the address bus](#)

Xiaotong Zhuang, Tao Zhang, Santosh Pande
December   ACM SIGARCH Computer Architecture News, Volume 32 Issue 5
2004
Publisher: ACM

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Keywords: address bus leakage protection, secure processor

4 [HIDE: an infrastructure for efficiently protecting information leakage on the address bus](#)

Xiaotong Zhuang, Tao Zhang, Santosh Pande
October   ASPLOS-XI: Proceedings of the 11th international conference on
2004   Architectural support for programming languages and operating systems
Publisher: ACM

Full text available: pdf(216.31 KB)   Additional Information: full citation, abstract, references, cited by, index terms

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Keywords: address bus leakage protection, secure processor

5 HIDE: an infrastructure for efficiently protecting information leakage on the address bus

Xiaotong Zhuang, Tao Zhang, Santosh Pande
November   ACM SIGPLAN Notices,   Volume 39 Issue 11
2004
Publisher: ACM

Full text available: pdf(216.31 KB)   Additional Information: full citation, abstract, references, cited by, index terms

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XOM-based secure processor has recently been introduced as a mechanism to provide copy and tamper resistant execution. XOM provides support for encryption/ decryption and integrity checking. However, neither XOM nor any other current approach adequately ...

Keywords: address bus leakage protection, secure processor

6 Drm to counter side-channel attacks?

Ryad Benadjila, Olivier Billet, Stanislas Francfort
October   DRM '07: Proceedings of the 2007 ACM workshop on Digital Rights
2007   Management
Publisher: ACM

Full text available: pdf(238.48 KB)   Additional Information: full citation, abstract, references, index terms

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In the DRM setting, the attacker is a very powerful adversary, owning the software as well as the underlying hardware. This context is far different from the black-box attacker commonly considered in conventional cryptography.Therefore, cryptographers ...

Keywords: AES, DRM, side-channel attacks, white-box

7 Code protection for resource-constrained embedded devices

H. Saputra, G. Chen, R. Brooks, N. Vijaykrishnan, M. Kandemir, M. J. Irwin
July 2004 ACM SIGPLAN Notices,   Volume 39 Issue 7
Publisher: ACM

Full text available: pdf(290.95 KB)   Additional Information: full citation, abstract, references, index terms

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While the machine neutral Java bytecodes are attractive for code distribution in the highly heterogeneous embedded domain, the well-documented and standardized features also make it difficult to protect these codes. In fact, there are several tools to ...

Keywords: Java security, cryptography, java byte code, mono-alphabetic, poly-alphabetic, substitution

8 Code protection for resource-constrained embedded devices

H. Saputra, G. Chen, R. Brooks, N. Vijaykrishnan, M. Kandemir, M. J. Irwin
June 2004   LCTES '04: Proceedings of the 2004 ACM SIGPLAN/SIGBED conference on Languages, compilers, and tools for embedded systems
Publisher: ACM

Full text available: pdf(290.95 KB)   Additional Information: full citation, abstract, references, index terms

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